Amendments to the Claims:

This listing of claims will replace all prior versions, and listings, of claims in the application:

1. (Currently amended) An RFID tag comprising:

a decoder for decoding a first received wireless signal subject to a reception bandwidth setting; and

a selector switch for transitioning from the first setting to a second different setting responsive to the first decoded signal to control further decoding by the decoder of a second subsequently received second signal.

2. (Original) The tag of claim 1, further comprising:

a filter, and

wherein the selector switch adjusts a bandwidth of the filter.

3. (Original) The tag of claim 2, wherein the filter is an active filter.

4. (Original) The tag of claim 2, wherein the filter is a passive filter.

5. (Original) The tag of claim 2, wherein the bandwidth is adjustable continuously over a range.

6. (Original) The tag of claim 2, wherein the filter includes at least two of a resistance, a capacitance and an inductance, and

at least one of the resistance, the capacitance and the inductance can be switched on and off.

7. (Original) The tag of claim 2, wherein the filter includes a capacitor and a switch switched at a variable rate.

8. (Original) The tag of claim 2, wherein the filter includes a resonator.

9. (Original) The tag of claim 1, further comprising:

a plurality of filters in possible paths of the received signal, and

wherein the selector switch routes the received first and second signal through different ones of the paths.

10. (Original) The tag of claim 1, wherein the decoder is adapted to generate a decoded signal responsive to decoding the first signal, and

the selector switch is adapted to transition to the different setting responsive to the first decoded signal.

11. (Currently amended) The tag of claim 1 [[10]], wherein the decoder is further adapted to generate a trigger signal responsive to decoding the first signal ~~from the decoded signal~~,

and further comprising:

a filter bandwidth adjuster adapted to control the selector switch responsive to the trigger signal.

12. (Original) The tag of claim 11, wherein the decoder is further adapted to compare the decoded signal to a preset code, and generate the trigger signal responsive to the comparison.

13. (Original) The tag of claim 12, wherein the preset code is at least a portion of a preamble.

14. (Original) The tag of claim 13, wherein the selector switch is adapted to transition to the different setting if the decoded signal matches the preset code.

15. (Original) The tag of claim 12, wherein the selector switch is adapted to transition to the different setting if the decoded signal does not match the preset code.

16. (Original) The tag of claim 12, wherein the selector switch is adapted to transition to the different setting if the decoded signal does not match the preset code after a preset time period.

17. (Original) The tag of claim 16, wherein the preset time period is two preamble durations.

18. (Original) The tag of claim 12, wherein the decoder further determines an active data rate of the first signal, and

encodes the active data rate in the trigger signal.

19. (Original) The tag of claim 18, wherein the active data rate is determined by determining a bit period between successively received symbols of the decoded first signal.

20. (Original) The tag of claim 18, wherein the decoded first signal is a preamble that has a preassociated data rate, and

the active data rate is determined from the preassociated data rate.

21. (Original) The tag of claim 18, wherein the preset code is a DATA RATE command,

the decoded first signal is the DATA RATE command with an associated data rate instruction, and

the active data rate is determined from the instruction.

22. (Original) A device comprising:

means for receiving a first and a second wireless signal;

means for decoding the first signal subject to a first reception bandwidth setting and the second signal subject to a second reception bandwidth setting different from the first setting; and

means for transitioning to the second setting from the first setting.

23. (Original) The device of claim 22, wherein transitioning is performed responsive to the first decoded signal.

24. (Original) The device of claim 23, further comprising:

means for comparing the decoded first signal with a preset code; and

means for determining whether to transition or not to the second setting depending on the comparison.

25. (Original) The device of claim 24, wherein if it is determined to not transition, the second signal is decoded subject to the first setting instead of the second setting.

26. (Original) The device of claim 24, wherein the preset code is at least a portion of a preamble.

27. (Original) The device of claim 24, wherein it is determined to transition if the decoded first signal does not match the preset code.

28. (Original) The device of claim 27, wherein it is determined to transition if the decoded first signal does not match the preset code after a preset waiting time.

29. (Original) The device of claim 28, wherein the preset waiting time is approximately two preamble durations.

30. (Original) The device of claim 24, wherein it is determined to transition if the decoded first signal matches the preset code.

31. (Original) The device of claim 22, wherein a plurality of reception bandwidth settings are provided, and

the second setting is the one of the plurality that is associated with the largest available reception bandwidth.

32. (Original) The device of claim 31, wherein the first setting is associated with a bandwidth of approximately 50kHz.

33. (Original) The device of claim 22, wherein the second setting depends on the first setting.

34. (Original) The device of claim 33, wherein a plurality of reception bandwidth settings are provided, and

the second setting is the one of the plurality that is associated with the incrementally next smaller available bandwidth than the first setting.

35. (Original) The device of claim 22, further comprising:

means for determining an active data rate from the decoded first signal, and

wherein a plurality of reception bandwidth settings are provided, and

the second setting is the one of the plurality that is associated with a bandwidth that best fits the active data rate.

36. (Original) The device of claim 35, wherein the active data rate is determined by determining a bit period between successively received symbols of the decoded first signal.

37. (Original) The device of claim 35, wherein the decoded first signal is a preamble that has a preassociated data rate, and

the active data rate is determined from the preassociated data rate.

38. (Original) The device of claim 35, wherein the decoded first signal is a DATA RATE command with an associated data rate instruction, and

the active data rate is determined from the instruction.

39. (Original) The device of claim 22, wherein transitioning is performed by adjusting a bandwidth of a filter.

40. (Original) The device of claim 22, wherein transitioning is performed by changing a path of the second received signal compared to the first received signal.

41. (Original) The device of claim 40, wherein the signal path includes a first filter, and transitioning routes the signal through a second filter.

42. (Original) A method for an RFID tag, comprising:

receiving a first wireless signal;

decoding the first signal subject to a first reception bandwidth setting;

transitioning to a second reception bandwidth setting different from the first setting;

receiving a second wireless signal; and

decoding the second signal subject to the second setting.

43. (Original) The method of claim 42, wherein transitioning is performed responsive to the first decoded signal.

44. (Currently amended) The method of claim 43 [[34]], further comprising:

comparing the decoded first signal with a preset code; and

determining whether to transition or not to the second setting depending on the comparison.

45. (Original) The method of claim 44, wherein if it is determined to not transition, the second signal is decoded subject to the first setting instead of the second setting.

46. (Original) The method of claim 44, wherein the preset code is at least a portion of a preamble.

47. (Original) The method of claim 44, wherein it is determined to transition if the decoded first signal does not match the preset code.

48. (Original) The method of claim 47, wherein it is determined to transition if the decoded first signal does not match the preset code after a preset waiting time.

49. (Original) The method of claim 48, wherein the preset waiting time is approximately two preamble durations.

50. (Original) The method of claim 44, wherein it is determined to transition if the decoded first signal matches the preset code.

51. (Original) The method of claim 50, further comprising:

transitioning to a third reception bandwidth setting different from the second setting;

receiving a third signal; and

decoding the third signal subject to the third setting.

52. (Original) The method of claim 42, wherein a plurality of reception bandwidth settings are provided, and

the second setting is the one of the plurality that is associated with the largest available reception bandwidth.

53. (Original) The method of claim 52, wherein the first setting is associated with a bandwidth of approximately 50kHz.

54. (Original) The method of claim 42, wherein the second setting depends on the first setting.

55. (Original) The method of claim 54, wherein a plurality of reception bandwidth settings are provided, and

the second setting is the one of the plurality that is associated with the incrementally next smaller available bandwidth than the first setting.

56. (Original) The method of claim 42, further comprising:

determining an active data rate from the decoded first signal, and

wherein a plurality of reception bandwidth settings are provided, and

the second setting is the one of the plurality that is associated with a bandwidth that best fits the active data rate.

57. (Original) The method of claim 56, wherein the active data rate is determined by determining a bit period between successively received symbols of the decoded first signal.

58. (Original) The method of claim 56, wherein the decoded first signal is a preamble that has a preassociated data rate, and

the active data rate is determined from the preassociated data rate.

59. (Original) The method of claim 56, wherein the decoded first signal is a DATA RATE command with an associated data rate instruction, and

the active data rate is determined from the instruction.

60. (Original) The method of claim 42, wherein transitioning is performed by adjusting a bandwidth of a filter.

61. (Original) The method of claim 42, wherein transitioning is performed by changing a path of the second received signal compared to the first received signal.

62. (Original) The method of claim 61, wherein the signal path includes a first filter, and transitioning routes the signal through a second filter.

63. (New) A circuit for an RFID tag comprising:

a decoder for decoding a first received wireless signal subject to a first reception bandwidth setting; and a selector switch for transitioning from the first setting to a second different setting responsive to the first decoded signal for further decoding by the decoder of a second subsequently received second signal subject to the second setting.

64. (New) The circuit of claim 63, further comprising:

a filter, and

wherein the selector switch adjusts a bandwidth of the filter.

65. (New) The circuit of claim 64, wherein the filter is an active filter.

66. (New) The circuit of claim 64, wherein the filter is a passive filter.

67. (New) The circuit of claim 64, wherein the bandwidth is adjustable continuously over a range.

68. (New) The circuit of claim 64, wherein the filter includes at least two of a resistance, a capacitance and an inductance, and

at least one of the resistance, the capacitance and the inductance can be switched on and off.

69. (New) The circuit of claim 64, wherein the filter includes a capacitor and a switch switched at a variable rate.

70. (New) The circuit of claim 64, wherein the filter includes a resonator.

71. (New) The circuit of claim 63, further comprising:

a plurality of filters in possible paths of the received signal, and

wherein the selector switch routes the received first and second signal through different ones of the paths.

72. (New) The circuit of claim 63, wherein the decoder is adapted to generate a decoded signal responsive to decoding the first signal, and

the selector switch is adapted to transition to the different setting responsive to the first decoded signal.

73. (New) The circuit of claim 63, wherein the decoder is further adapted to generate a trigger signal responsive to decoding the first signal,

and further comprising:

a filter bandwidth adjuster adapted to control the selector switch responsive to the trigger signal.

74. (New) The circuit of claim 73, wherein the decoder is further adapted to compare the decoded signal to a preset code, and generate the trigger signal responsive to the comparison.

75. (New) The circuit of claim 74, wherein the preset code is at least a portion of a preamble.

76. (New) The circuit of claim 75, wherein the selector switch is adapted to transition to the different setting if the decoded signal matches the preset code.

77. (New) The circuit of claim 74, wherein the selector switch is adapted to transition to the different setting if the decoded signal does not match the preset code.

78. (New) The circuit of claim 74, wherein the selector switch is adapted to transition to the different setting if the decoded signal does not match the preset code after a preset time period.

79. (New) The circuit of claim 78, wherein the preset time period is two preamble durations.

80. (New) The circuit of claim 74, wherein the decoder further determines an active data rate of the first signal, and

encodes the active data rate in the trigger signal.

81. (New) The circuit of claim 80, wherein the active data rate is determined by determining a bit period between successively received symbols of the decoded first signal.

82. (New) The circuit of claim 80, wherein the decoded first signal is a preamble that has a preassociated data rate, and

the active data rate is determined from the preassociated data rate.

83. (New) The circuit of claim 80, wherein the preset code is a DATA RATE command, the decoded first signal is the DATA RATE command with an associated data rate instruction, and the active data rate is determined from the instruction.